

REMARKS

At the outset, Applicants thank the Examiner for examining the pending application. The Office Action dated July 13, 2007 has been received and its contents have been carefully reviewed.

Summary of the Office Action:

Claims 1-3, 6, and 9-12 are rejected. The Office Actions rejects claims 1-3, 6, and 9-12 under 35 U.S.C. 103(a) as being unpatentable over Yasuda et al. (US 4,842,371 A) in view of Saishu et al. (US 5,949,391 A).

Summary of the Response to the Office Action:

Applicants have amended claims 1 and 9, and canceled claims 3, 6 and 11 to further define the invention. No new matter has been added. Reconsideration of the pending claims are respectfully requested.

Rejection Under 35 U.S.C 103(a):

Claim 1 is allowable over the cited references in that claim 1 recites a combination of elements including, for example, "a data driving circuit for controlling opposite polarity voltages for electric field alignment supplied to the adjacent data lines during the electric field alignment, and wherein the turn-ON voltage is supplied the gate lines in a range of from ten to four-hundred times during the electric field alignment".

The present invention supplies voltages for electric field alignment during an electric field alignment. The voltages for electric field alignment are fully supported by at least paragraphs [0067] and [0068]. That is, a timing controller supplies an electric field alignment data signal (EFD) to a data driving circuit 81 during an electric field alignment. The data driving

circuit 81 converts the electric field alignment data signal supplied from the timing controller 80 into an analog gamma voltage during the electric field alignment. The analog gamma voltage means the voltages for electric field alignment. Also, the timing controller 8 supplies a R, G, and B digital video data to the data driving circuit during a normal driving for displaying substantially an image. Thus, the voltages for electric field alignment are basically different from the R, G, and B digital video data. In contrast, an odd- and even-numbered field color signals of Yasuda (Column 8, Line 67 - Column 9, Line 49) represents R, G, and B digital video data for displaying substantially an image during a normal driving.

In addition, the present invention supplies a turn-ON voltage to a gate lines in a range of from ten to four-hundred times during the electric field alignment. Yasuda, as indicted by the Examiner relating to claim 3, merely discloses in Fig. 10 and Column 11, Lines 54-62 a vertical synchronizing signal. In general, the vertical synchronizing signal is a clock signal for driving a LCD. Thus, Yasuda dose not disclose “supplying a turn-ON voltage to a gate lines in a range of from ten to four-hundred times during the electric field alignment” of the claimed invention.

Therefore, none of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that claim 1 and claim 2, which depends therefrom, are allowable over the cited references.

Claim 9 is allowable over the cited reference in that claim 9 recites a combination of elements including for example, “a data driving circuit for controlling opposite polarity voltages for electric field alignment supplied to the adjacent data lines during the electric field alignment, and wherein the turn-ON voltage is supplied the gate lines in a range of from ten to four-hundred times during the electric field alignment”.

The present invention supplies voltages for electric field alignment during an electric field alignment. The voltages for electric field alignment are fully supported by at least paragraphs [0067] and [0068]. That is, a timing controller supplies an electric field alignment data signal (EFD) to a data driving circuit 81 during an electric field alignment. The data driving circuit 81 converts the electric field alignment data signal supplied from the timing controller 80 into an analog gamma voltage during the electric field alignment. The analog gamma voltage means the voltages for electric field alignment. Also, the timing controller 8 supplies a R, G, and B digital video data to the data driving circuit during a normal driving for displaying substantially an image. Thus, the voltages for electric field alignment are basically different from the R, G, and B digital video data. In contrast, an odd- and even-numbered field color signals of Yasuda (Column 8, Line 67 – Column 9, Line 49) represents R, G, and B digital video data for displaying substantially an image during a normal driving.

In addition, the present invention supplies a turn-ON voltage to a gate lines in a range of from ten to four-hundred times during the electric field alignment. Yasuda, as indicted by the Examiner relating to claim 3, merely discloses in Fig. 10 and Column 11, Lines 54-62 a vertical synchronizing signal. In general, the vertical synchronizing signal is a clock signal for driving a LCD. Thus, Yasuda dose not disclose “supplying a turn-ON voltage to a gate lines in a range of from ten to four-hundred times during the electric field alignment” of the claimed invention.

Therefore, none of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicants respectfully submit that claim 9 and claims 10-12, which depend therefrom, are allowable over the cited references.

CONCLUSION

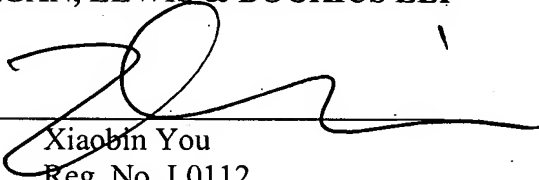
In view of the foregoing amendments and remarks, Applicants respectfully request the reconsideration and the timely allowance of the pending claims. Should the Examiner believe that there are any issues outstanding after consideration of this response, the Examiner is invited to contact Applicants' undersigned representative to expedite prosecution.

If there are any other fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-0310. If a fee is required for an extension of time under 37 C.F.R. § 1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully submitted,

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By: _____


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